## ANNA UNIVERSITY, CHENNAI AFFILIATED INSTITUTIONS B.E. ELECTRONICS AND COMMUNICATION ENGINEERING REGULATIONS – 2017 CHOICE BASED CREDIT SYSTEM

## MAPPING OF COURSE OUTCOMES WITH PROGRAMME OUTCOMES:

A broad relation between the Course Outcomes and Programme Outcomes is following table

COURSE OUTCOMES			PROGRAMME OUTCO									
Sem	Course Name	а	b	С	d	е	f	g	h			
1	Communicative English						V	V	V			
	Engineering Mathematics – I	V	V	<b>√</b>	V				П			
	Engineering Physics	V	V	<b>√</b>	V							
	Engineering Chemistry	V	V	V	V							
	Problem Solving and Python Programming	<b>√</b>	V	<b>√</b>	V	V			$\Box$			
	Engineering Graphics	<b>√</b>										
	Problem Solving and Python Programming Laboratory	<b>√</b>	٧	<b>V</b>	<b>√</b>	<b>√</b>						
	Physics and Chemistry Laboratory	V	V	V	V							
	Technical English					V	V	V	<b>V</b>			
	Engineering Mathematics – II	<b>√</b>	V	<b>√</b>	V							
	Physics for Electronics Engineering	V	V	V	V							
п	Basic Electrical and Instrumentation Engineering	<b>√</b>	1	<b>V</b>	٧	<b>V</b>	٧					
	Circuit Analysis	V	V	V	V	V	V					
	Electronic Devices	V	V	V	V	V	V					
	Circuits and Devices Laboratory	V	V	V	V	V						
	Engineering Practices Laboratory	V	V	V	V	V						
	Linear Algebra and Partial Differential Equations	<b>√</b>	1	<b>√</b>	٧	<b>√</b>						
	Fundamentals of Data Structures In C	V	V	V	V	V	1					
	Electronic Circuits- I	V	V	V	V	V	1					
III	Signals and Systems	V	V	V	V	V	V					
	Digital Electronics	V	V	V	V	V	1					
	Control System Engineering	V	V	V	V	V	V					
	Fundamentals of Data Structures in C Laboratory	<b>√</b>	٧	<b>V</b>	<b>√</b>	1	<b>V</b>					
	Analog and Digital Circuits Laboratory	V	V	V	V	V	V					
	Interpersonal Skills/Listening &Speaking						٧		V			
IV	Probability and Random Processes	1	<b>√</b>	<b>√</b>	<b>√</b>	V			$\vdash$			
	Electronic Circuits II	V	V	V	V	V	<b>√</b>		$\Box$			
	Communication Theory	V	V	<b>√</b>	V	V	1					
	Electromagnetic Fields	V	V	√	V	V	<b>V</b>					
	Linear Integrated Circuits	V	V	V	V	V	1					
	Environmental Science and Engineering	V	V		V		V		V			

	COURSE OUTCOMES				PROGRAMME									
Sem	Course Name	а	b	С	d	е	f							
	Circuits Design and Simulation Laboratory	<b>√</b>	<b>√</b>	1	<b>√</b>	1	<b>√</b>							
	Linear Integrated Circuits Laboratory	√	1	1	<b>√</b>	1	<b>√</b>							
		$\top$					$\Box$							
	Digital Communication	V	1	1	1	1	<b>√</b>							
	Discrete-Time Signal Processing	V	<b>V</b>	1	1	V	<b>√</b>							
	Computer Architecture and Organization	V	1	V	1		<b>√</b>							
	Communication Networks	V	<b>V</b>	V	1	V	<b>√</b>							
V	Professional Elective I													
	Open Elective I						$\Box$							
	Digital Signal Processing Laboratory	√	<b>V</b>	<b>V</b>	1	1	<b>√</b>							
	Communication Systems Laboratory	V	1	V	1	1	<b>√</b>							
	Networks Laboratory	1	<b>√</b>	<b>√</b>	1	V	<b>√</b>							
		1												
	Microprocessors and Microcontrollers	V	<b>V</b>	<b>V</b>	1	1	<b>√</b>							
	VLSI Design	V	V	V	V	V	<b>√</b>							
	Wireless Communication	V	V	V	V	V	<b>√</b>							
VI	Principles of Management	+		<u> </u>	<u> </u>	,	<b>√</b>							
	Transmission Lines and RF Systems	1	V	V	V	V	<b>√</b>							
	Professional Elective -II	+		<u> </u>	Ė	<u> </u>								
	Microprocessors and Microcontrollers	1,	1	.,	.,	.,	,							
	Laboratory	1	٧	٧	٧	٧	٧							
	VLSI Design Laboratory	V	1	V	1	V	<b>√</b>							
	Technical Seminar		1		1	1	<b>√</b>							
	Professional Communication						<b>√</b>							
VII	Antennas and Microwave Engineering	V	V	V	V	V	<b>√</b>							
	Optical Communication	1	1	V	1		<b>√</b>							
	Embedded and Real Time Systems	V	1	V	1	1	<b>√</b>							
	Ad hoc and Wireless Sensor Networks	V	1	V	1	V	<b>√</b>							
	Professional Elective -III													
	Open Elective - II													
	Embedded Laboratory	V	V	V	V	1	<b>√</b>							
	Advanced Communication Laboratory	V	V	V	V	V	<b>√</b>							
VIII	Professional Elective - IV													
	Professional Elective - V													
	Project Work	V	V	V		V	1							